


07/13/01

	Class	Subclass
ISSUE CLASSIFICATION		

**PATENT NUMBER**

## U.S. UTILITY Patent Application

<div style="display: flex; justify-content: space-between;"> <div> <i>JP</i>              SCANNED <u>TRI</u> Q.A. <u>Gu</u> </div> <div>           O.I.P.E.         </div> </div>	PATENT DATE
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APPLICATION NO. 09/903792	CONT/PRIOR F	CLASS <u>430</u> <del>430</del>	SUBCLASS <u>313</u> <del>2042</del>	ART UNIT <del>2042</del>	EXAMINER <del>DUDA</del> Sagon
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## APPLICANTS

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Keishiro Amaya

1756

**TITLE**

Conductor pattern and electronic component having the same

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) _____ (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	
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